IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Machala III, et al.

Art Unit:

1763

Serial No.:

10/735,229

Examiner:

Goudreau, G.

Filing Date:

12/11/2003

Docket No.: TI-36674

Customer No.:

23494

Conf. No.:

1860

Title: METHOD FOR IMPROVING TRANSISTOR LEAKAGE CURRENT

UNIFORMITY

REQUEST FOR EXTENSION OF TIME

Commissioner of Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facelmile to the U.S. Patent and Trademark Office at 703-872-9306 on

e J. Gamer, Reg. No. 36,144

May 2, 2005

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

☐ One Month (\$120)

☐ Two Months (\$450)

☐ Three Months (\$1,020)

Four Months (\$1,590)

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668.

Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 Fax: (972) 917-4418

Respectfully_Submitted.

Sacqueline J. Garner Reg. No. 36,144

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